

Model	Size	Detection axis	Interface	Supply voltage	Operation Temperature	Scale factor	Bias	Rate range
	[mm]			[V]	[°C]	[%·s] Typ.	[%·s] Typ. @+25°C	[%/s]
XV-8000CB	5.0 x 3.2 x 1.3	1	Analog Voltage	5	-40 to +85	0.005 x VDD mv	0.5 x VDD V	+/-60
(AEC-Q200)								
XV-8000LK	6.0 x 4.8 x 3.3	1	Analog Voltage	5	-40 to +85	0.005 x VDD mv	0.5 x VDD V	+/-60
(AEC-Q200)								
XV-9100CD	5.0 x 5.0 x 1.4	1	Analog Voltage	5	-40 to +105	0.004 x VDD mV	0.5 x VDD V	+/-100
(AEC-Q100)								
XV-9100LP	9.5 x 5.0 x 7.2	1	Analog Voltage	5	-40 to +125	0.004 x VDD mV	0.5 x VDDV	+/-100
(AEC-Q100)								
XV-9100LV	7.0 x 6.8 x 3.3	1	Analog Voltage	5	-40 to +125	0.004 x VDD mV	0.5 x VDD V	+/-100
(AEC-Q100)								
XV-9300CD	5.0 x 5.0 x 1.4	1	Analog Voltage	5	-40 to +105	0.0012 x VDD mV	0.5 x VDD V	+/-300
(AEC-Q100)								
XV-9300LP	9.5 x 5.0 x 7.2	1	Analog Voltage	5	-40 to +125	0.0012 x VDD mV	0.5 x VDD V	+/-300
(AEC-Q100)								
XV-9300LV	7.0 x 6.8 x 3.3	1	Analog Voltage	5	-40 to +125	0.0012 x VDD mV	0.5 x VDD V	+/-300
(AEC-Q100)								
XV4001BC	5.0 x 3.2 x 1.3	1	I2C	3.3	-40 to +85	370LSB +/-1.5%	+/-2 (0 LSB Typ.)	+/-70
(AEC-Q200)								
XV4001BD	5.0 x 3.2 x 1.3	1	SPI	3.3	-40 to +85	370LSB +/-1.5%	+/-2 (0 LSB Typ.)	+/-70
(AEC-Q200)								
XV4001KC	6.0 x 4.8 x 3.3	1	I2C	3.3	-40 to +85	370LSB +/-1.5%	+/-2 (0 LSB Typ.)	+/-70
(AEC-Q200)								
XV4001KD	6.0 x 4.8 x 3.3	1	SPI	3.3	-40 to +85	370LSB +/-1.5%	+/-2 (0 LSB Typ.)	+/-70
(AEC-Q200)								